



2183

Attorney's Docket No. 42390P10924
Confirmation No.: 3051

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Srinivas Chennupaty, et al.

Application No.: 09/783,779

Filed: February 14, 2001

For: SHUFFLE INSTRUCTIONS

Art Unit: 2183

Examiner: Not yet assigned

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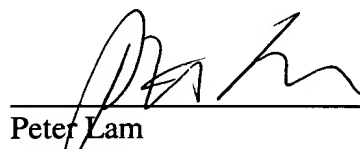
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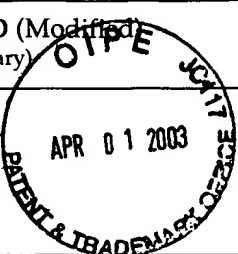
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Page 1 of 1		First Named Inventor: Srinivas Chennupati, et al.	Examiner: Not yet assigned
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Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ⁶
		ABBOTT, et al., "Broadband Algorithms with the MicroUnity Mediaprocessor," MicroUnity Systems Engineering, Inc., Proceedings of Compcon, IEEE, 1996, pp. 349-354.	
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		DIEFENDORFF, K., et al., "Altivec Extension to PowerPC Accelerates Media Processing," IEEE, #0272-1732/00, 2000, pp. 85-95.	
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		HAYES, et al., "MicroUnity Software Development Environment," MicroUnity Systems Engineering, Inc., Proceedings of Compcon, IEEE, 1996, pp. 341-348.	
		INTEL CORPORATION, "Intel Architecture Software Developer's Manual, Vol. 2; Instruction Set Reference," 1999, 26 pgs.	
		INTEL CORPORATION, "Willamette Processor Developer's Guide," Manual, Feb. 2000, 16 pages.	
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		INTEL CORPORATION, "Intel® Itanium™ Architecture Software Developer's Manual, Vol. 3: Instruction Set Reference," Rev. 2.0, December 2001, 30 pgs. total.	
		INTEL CORPORATION, "Intel486™ Microprocessor Family Programmer's Reference Manual," 1992, 44 pgs. total.	
		INTEL CORPORATION, "Pentium® Processor Family Developer's Manual, Vol. 3: Architecture and Programming Manual," 1995, 54 pgs. total.	
		INTEL CORPORATION, "Pentium® Processor User's Manual, Vol. 3: Architecture and Programming Manual," 1993, 50 pgs. total.	
		LEVINTHAL, et al., "Chap - A SIMD Graphics Processor," Computer Graphics Project, ACM, Vol. 18, No. 3, July 1984, pp. 77-81.	
		LEVINTHAL, et al., "Parallel Computers for Graphics Applications," Proceedings: Second Int'l. Conf. on Architectural Support for Programming Languages and Operating Systems, (ASPLOS II), IEEE, 1987, pp. 193-198.	
		WANG, et al., "A Processor Architecture for 3D Graphics Calculations," Computer Motion, Inc., Goleta, CA, 23 pgs.	

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